




FACULTY PROFILE

1. Name		INDUMATHI T.S				
2. Date of Birth & Age		03.05.1966 42 YRS				
3. Educational qualifications		M.E				
(High school to Highest Qualification)						
Designation		ASST PROFESSOR				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	BANGALORE UNIVERSITY	S.I.T TUMKUR	ELECTRONICS	1988		
b. P.G.Degree	BANGALORE UNIVERSITY	UVCE BANGALORE	ELECTRONICS	1992		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
e. CEP of IISC	Introduction to MEMS and applications (5 months)					
4. Date of Joining and No of years of Experience						
Work experience		Teaching	Research	Industry	Others	Total
		15y 10m	----	-----	---	15yrs 10 months
5. Technical/computer training under went(place of institute)						
a) before joining duty						
b) after joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	TELECOMMUNICATION & SWITCHING					
	DIGITAL COMMUNICATION					
	SIGNALS & SYSTEM					
	INFORMATION THEORY & CODING					
Practicals :	COMMUNICATION LAB, MICROWAVE LAB					
	ANALOG & DIGITAL ELECTRONICS LAB					
	MICROPROCESSOR LAB					
Project work :	IN THE AREA OF COMMUNICATION, NETWORKS					
7. Research guidance to no of students Groups	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/seminar		
	-----	----	-----	04		
8. Research Publications						
9. No. of books published with details						
-						
10. Projects carried out						
AICTE-MODROB 2nos						
11. Technology transfer						
-						
12. Patents						
-						


FACULTY PROFILE

1. Name		RAJANNA K.M.				
2. Date of Birth & Age		10 th MAY 1967 41 yrs				
3. Educational qualifications		M.E (DIGITAL COMMUNICATION)				
Designation		ASST PROFESSOR				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	MYSORE UNIVERSITY	NIE, MYSORE	ELECTRONICS & COMMUNICATION	1995		
b. P.G.Degree	BANGALORE UNIVERSITY	BMSCE, BANGALORE	DIGITAL COMMUNICATION	2001		
c. Ph.D	-	-	-	-		
d. CEP of IISc	1. Microprocessors and interfacing Techniques 2. Programming in C					
4. Date of Joining and No of years of Experience						
Work experience		Teaching	Research	Industry	Others	Total
7 th Feb'2003, 4YRS 5M		11Y 7M				11Y 7 M
5. Technical/computer Training under went(Place of Institute)						
a) Before joining duty						
b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	MICROPROCESSORS, MICROCONTROLLER & APPLICATIONS, DIGITAL SIGNAL PROCESSING, EMBEDDED CONTROLLER, EMBEDDED SYSTEM DESIGN(UG/PG)					
Practicals :	MICROPROCESSORS LAB, DIGITAL ELECTRONICS LAB, ANALOG LAB, VHDL, COMMUNICATION LAB, DSP LAB (MATLAB), ADVANCED MICROPROCESSOR LAB					
Project work :	GUIDING BOTH UG &PG STUDENTS					
7. Research guidance Nos.						
-	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/seminar/ attended		
	-	01	01	09		
8. Research Publications		NIL				
9. No. of books published with details		NIL				
10. Projects carried out		NIL				
11. Technology transfer		NIL				
12. Patents		NIL				


FACULTY PROFILE

1. Name		MAHESAN K.V.M				
2. Date of Birth & Age		15.04.1964 43 YRS				
3. Educational qualifications (High school to Highest Qualification)		SSLC, PUC,BSC,B.E, M.TECH				
Designation		SL.Grade LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	B.E BANGALORE UNIVERSITY	SSIT, TUMKUR	ELECTRONICS	1990		
b. P.G.Degree	M.TECH, VTU	SJCE , MYSORE	INDUSTRIAL ELECTRONICS	2001		
c. Ph.D	-	-	-			
d. Others of any	BSc KERALA UNIVERSITY	S. N COLLEGE, KOLLAM	MATHEMATICS	1985		
4. Date of joining and No of Years of experience						
Work experience		Teaching	Research	Industry	Others	Total
14-09-1992		15YRS 8M	NIL	NIL	NIL	NIL
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	POWER ELECTRONICS, ELECTRONIC CIRCUITS, MOBILE COMMUNICATION, GSM DIGITAL PRINCIPLES, SATELLITE COMMUNICATION, COMMUNICATION CHANNELS					
Practicals :	POWER ELECTRONICS LAB, MICROWAVE LABS, ELECTRONIC CIRCUITS LAB					
Project work :	COMMUNICATION BASED PROJECTS, ATM BASED PROJECTS					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/seminars/ attended		
NIL	NIL	NIL	NIL	3		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents		-				


FACULTY PROFILE

1. Name		YAMUNA DEVI C.R				
2. Date of Birth & Age		18.05.1969		39yrs		
3. Educational qualifications (High school to Highest Qualification)		B.E, M.S				
Designation		SELECTION GRADE LECTURER			Signature	
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	BU, Bangalore	UVCE, BANGALORE	COMPUTER SCIENCE	1990		
b. P.G.Degree	B ITS	BITS, PILANI	SOFTWARE SYSTEMS	1998		
c. Ph.D	-	-	-	-		
d. Others if any	-	-	-	-		
4 Date of joining and No of Years of experience						
Work experience		Teaching	Research	Industry	Others	Total
		16 yrs				
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory		DATA STRUCTURE USING C++, OPERATING SYSTEMS, OPERATION RESEARCH & ENGINEERING MANAGEMENT, LOGIC DESIGN, ADVANCED MICROPROCESSOR.				
Practicals :		VHDL LAB , DIGITAL ELECTRONICS LAB. 8085 MICROPROCESSORS LAB ADVANCED MICROPROCESSOR LAB				
Project work :		FUZZY LOGIC BASED EFFECTIVE CONGETION CONTROL SYSTEM MINI DATA LOGGER APPLICATION ON ETHERNET ADOPTER				
7. Research guidance Nos.		No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/Seminars/ attended	
8. Research Publications			03	-	3	
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents		-				


FACULTY PROFILE

1. Name		VIDYA HONGUNTIKAR					
2. Date of Birth & Age		26.1.1968				39 YRS	
3. Educational qualifications (High school to Highest Qualification)		B.E, M.E					
Designation		Scl.grade LECTURER					
				Signature			
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing			
a. UG Degree	GULBURGA UNIVERSITY	P.D.A COLLEGE OF ENGG	ELECTRONICS & COMMUNICATION	1990			
b. P.G.Degree	BANGALORE UNIVERSITY	UVCE	ELECTRONICS	1999			
c. Ph.D	-	-	-	-			
d. Others of any	-	-	-	-			
4. Date of Joining and No of years of Experience							
Work Experience		Teaching	Research	Industry	Others	Total	
		7/9/1992					
		15YRS 9M					
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty							
6. Subjects teaching at Under Graduate level / Post Graduate Level							
Under graduate Level							
Theory	COMPUTER COMMUNICATION NETWORKS, LINEAR INTEGRATED CIRCUITS BASIC ELECTRONICS						
	TRANSMISSION LINES & WAVEGUIDES						
	MICROPROCESSORS(8085), ANTENNA AND PROPAGATION, ITC						
Practicals :	CCN LAB , HDL LAB						
	MP LAB 8085						
	COMMUNICATION LAB U WAVE LAB						
Project work :	HIGH TECH CAR WITH RF CAMERA						
	AUTOMATIC SPEED BRAKER USING IR						
	CONTROLLING REMOTE NOTICE BOARD USING GSM WITH SMS						
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences			
-	-	-	-	4 PAPERS			
8. Research Publications		-					
9. No. of books published with details		-					
10. Projects carried out		-					
11. Technology transfer		-					
12. Patents		-					


FACULTY PROFILE

1. Name		SUDHA .H THIMMAIAH				
2. Date of Birth & Age		04.04.1969 39YRS				
3. Educational qualifications (High school to Highest Qualification)		M.E				
Designation		SR. LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	BANGALORE UNIVERSITY	B.M.S.C.E	TELECOMMUNICATION	1990		
b. P.G.Degree	ANNA UNIVERSITY	ANNA UNIVERSITY (COLLEGE OF ENGG,GUINDY)	MEDICAL ELECTRONICS	2000		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
4. Date of Joining and No of years of Experience						
Work Experience		Teaching	Research	Industry	Others	Total
12 TH AUG 1992		15 yrs	0Y 0M	NIL	1YR	15 yrs
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	MICROPROCESSORS 8085					
	ADVANCED MICROPROCESSORS 8086					
	SATELLITE COMMUNICATION					
Practicals :	MICROPROCESSORS 8085					
	ADVANCED MICROPROCESSORS 8086					
	MICROWAVE LAB & ANTENNA LAB					
Project work :	02					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/Seminars/ attended		
-	-	-	-	-		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents		-				


FACULTY PROFILE

1. Name		SHRUTHI P.C				
2. Date of Birth & Age		22.06.1979		28YRS		
3. Educational qualifications		B.E (TELECOMMUNICATION)				
(High school to Highest Qualification)						
Designation		LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	BE/BANGALORE UNIVERSITY	DR. AIT BANGALORE	TELECOMMUNICATION	2001		
b. P.G.Degree	-	-	-	-		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
4. Date of Joining and No of years of Experience						
Work Experience		Teaching	Research	Industry	Others	Total
6yrs		8.03.04 6YRS	10MONTHS (DRDO-GTRE)	-	-	6yrs
5. Technical/Computer Training under went (place of institute)						
a) Before joining duty						
b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	COMPUTER ORGANISATION, SOLID STATE DEVICE & TECHNOLOGY, ELECTRICAL & ELECTRONICS MEASUREMENT, MICROWAVE ENGG, EMBEDDED SYSTEM & DESIGN					
Practicals :	LOGIC DE LAB, MICROPROCESSORS LAB, MICROWAVE LAB, VHDL LAB,HDL LAB					
Project work :	GUIDED 4 PROJECTS					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences		
-	-	-	-	-		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents						


FACULTY PROFILE

1. Name		ARAVINDA H.L				
2. Date of Birth & Age		23.02.1982		26 Years		
3. Educational qualifications (High school to Highest Qualification)		B.E				
Designation		LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study		Area of specialization	Year of passing	
a. UG Degree	V.T.U	DR. AIT		TELECOMMUNICATION	2003	
b. P.G.Degree	V.T.U	RVCE		Computer Science	Persuing	
c. Ph.D	-	-		-	-	
d. Others of any	-	-		-	-	
4. Date of Joining and No of years of Experience						
Work Experience		Teaching	Research	Industry	Others	Total
		10 TH MAR 2004		-	-	5 yrs
5. Technical/Computer Training under went (place of institute)						
a) Before joining duty						
b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	NETWORK ANALYSIS					
	OPTICAL FIBER COMMUNICATION, INFORMATION THEORY & CODING, ANALOG COMMUNICATION, CONTROL SYSTEMS					
Practicals :	ADVANCED COMMUNICATION LAB, HDL LAB					
	POWER ELECTRONICS LAB					
	ANALOG ELECTRONICS LAB					
Project work :	GUIDED 4 PROJECTS					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences/Seminars		
-	-	-	-	-		
8. Research Publications	-					
9. No. of books published with details	-					
10. Projects carried out	-					
11. Technology transfer	1 PAPER					
12. Patents	-					


FACULTY PROFILE

1. Name		CHANDRAKALA.V				
2. Date of Birth & Age		17.04.1975	32 Yrs			
3. Educational qualifications (High school to Highest Qualification)		M.TECH				
Designation		LECTURER				
					Signature	
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	B.U	S.J.C.I.T CHICKBALLAPUR	E&C	1998		
b. P.G.Degree	VTU	B.M.S.C.E BANGALORE	DIGITAL COMMUNICATION	2003		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
4. Date of Joining and No of years of Experience						
Work Experience		Teaching	Research	Industry	Others	Total
6 ½ YRS		18.3.04				6 ½ yrs
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	FIELD THEORY, LOGIC DESIGN, ANALOG COMMUNICATION, ANTENNA THEORY, MICROWAVE ENGG,ELECTRICAL & ELECTRONIC MEASUREMENTS,RADAR SYSTEMS.					
Practicals :	DIGITAL ELECTRONICS LAB, COMMUNICATION LAB ,ADVANCED ANTENNA LAB, VHDL LAB, UP LAB, POWER ELECTRONICS LAB,CCP LAB, MICROWAVE LAB, CCN LAB,ANALOG ELECTRONICS LAB, ADVANCED COMMUNICATION LAB.					
Project work :	PANIC SYSTEMS & SECURITY					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences		
-	-	-	-	-		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents		-				

FACULTY PROFILE

1. Name		USHA RANI.M.A				
2. Date of Birth & Age		02.04.1979 28 Yrs				
3. Educational qualifications (High school to Highest Qualification)		S.S.L.C, PUC				
Designation		B.E LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	BANGALORE	S.I.T, TUMKUR	ELECTRONICS & COMMUNICATION	2001		
b. P.G.Degree	-	-	-	-		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
4. Date of Joining and No of years of Experience						
Work Experience		Teaching	Research	Industry	Others	Total
3 years		24.10.2005 Till DATE	12 months HAL			
5. Technical/Computer Training under went (place of institute) a) Before joining duty b) After joining duty						
6. Subjects teaching at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	CONTROL SYSTEM (IV SEM)					
	BASIC ELECTRONICS (I & II SEM)					
	SOLID STATE DEVICES AND TECHNOLOGY (V SEM)					
	COMPUTER ORGANIZATION (1V SEM)					
Practicals :	VHDL , P.E, MPU 8085, 8086 DIGITAL LAB ANALOG LAB,MICROCONTROLLERS,HDL					
Project work :	02					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11. Technology transfer		-				
12. Patents		-				

FACULTY PROFILE

1. Name		RADHIKA D.K				
2. Date of Birth & Age		17.02.1983 25YRS				
3. Educational qualifications		B.E				
(High school to Highest Qualification)						
Designation		LECTURER				
				Signature		
Qualifications	Board / IIT/ NIT/ University / others	Institute and place of study	Area of specialization	Year of passing		
a. UG Degree	VTU	DR. AIT BANGALORE	TELECOMMUNICATION	2004		
b. P.G.Degree	-	-	-	-		
c. Ph.D	-	-	-	-		
d. Others of any	-	-	-	-		
4. Work experience						
		Teaching	Research	Industry	Others	Total
Date of Joining and years		25/10/2005 TILL DATE				
3 yrs						3 yrs
5. Technical/Computer Training Underwent (Place of Institute)		Workshop on Matlab, Simulink and DSP (Dr. A.I.T.) Recent Trends in Advanced Communication Engg. (Dr. A.I.T.) National Workshop on HDL (EWIT)				
a) Before joining duty						
b) After joining duty						
6. Subjects taught at Under Graduate level / Post Graduate Level						
Under graduate Level						
Theory	FIELD THEORY (4 TH SEM), OPERATING SYSTEMS(7 TH SEM), CCP(1 ST SEM)					
	ANALOG COMMUNICATION (5 TH SEM),C++(6 TH SEM), HDL(4 TH SEM)					
	ELECTRONIC CIRCUITS (3 RD SEM)					
Practicals :	DIGITAL ELECTRONICS, ANALOG ELECTRONICS, MICROCONTROLLER, VHDL, MICROPROCESSOR 8086					
Project work :	01					
7. Research guidance Nos.	No. of Papers published in Master's / Ph.D level	National journals	International Journals	Conferences		
-	-	-	-	-		
8. Research Publications		-				
9. No. of books published with details		-				
10. Projects carried out		-				
11 Technology transfer		-				
12. Patents		-				